

ABSTRACT

An image processing apparatus capable of efficient rendering is provided. In an image processing apparatus 1000 which renders, in a screen coordinate system, unit figures each constituting the surface of a three-dimensional object to be rendered, a rasterizing unit 10 divides a rendering area corresponding to a screen into multiple unit areas, while a first unit figure is projected onto a screen coordinate system, and outputs the unit areas. A similar process is applied to second and subsequent unit figures so that the multiple unit areas constituting each unit figure are sequentially output. An area divider 20 divides each of the unit areas sequentially output from the rasterizing unit 10 into multiple subareas. An area discarder 30 discards as necessary a subarea obtained by the division by the area divider 20 according to a predetermined rule. An area writer 40 re-merges subareas that survived the discarding process by the area discarder 30 and writes merged areas obtained by re-merge in the memory.